

Ke Xia

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Research Interests

My research interests lie in hardware security, especially in enhancing the security of the heterogeneous architecture, such as the CPU-FPGA architecture. I am interested in hardware security, heterogeneous system, and disaggregated system.

Education

RUTGERS UNIVERSITY, ELECTRICAL & COMPUTER ENGINEERING DEPARTMENT | 2019 - PRESENT |

PHD STUDENT, COMPUTER ENGINEERING

- Advisor: Dr. Sheng Wei

RUTGERS UNIVERSITY, ELECTRICAL & COMPUTER ENGINEERING DEPARTMENT | 2017-2019 |

MASTER OF ENGINEERING, COMPUTER ENGINEERING

- Advisor: Dr. Sheng Wei
- GPA: 3.73/4.0

UNIVERSITY OF ELECTRONIC SCIENCE AND TECHNOLOGY OF CHINA | 2014-2018 |

BACHELOR OF ENGINEERING

- Major: Network Security Engineering
- GPA: 3.78/4.0

Publications

- Ke Xia, Yukui Luo, Xiaolin Xu and Sheng Wei, "SGX-FPGA: Trusted Execution Environment for CPU-FPGA Heterogeneous Architecture", DAC 2021
- Xianglong Feng, Mengmei Ye, Ke Xia, Sheng Wei, "Runtime Fault Injection Detection for FPGA-based DNN Execution Using Siamese Path Verification.", DATE 2021

Awards

- Student Travel Grant, IEEE International Symposium on Hardware Oriented Security and Trust (HOST), June 2022.
- Travel Grant, IEEE HOST – Women in Hardware and Systems Security Workshop (WISE), May 2019.

Teaching

RUTGERS UNIVERSITY, ELECTRICAL & COMPUTER ENGINEERING DEPARTMENT

TEACHING ASSISTANT

- “Computer Architecture” - Fall 2020
- “Computer Architecture & Assembly Code” - Fall 2021
- “Sustainable Energy” - Fall 2019

CO-MENTOR

- “Special Problem” for Undergraduate Level – Spring 2022